

A6 B/1-1304
u Jones

PATENT
Attorney Docket No. 401191

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE



In re Application of:

HATAKENAKA et al.

Application No. 09/871,978

Filed: June 4, 2001

Art Unit: 2133

Examiner: D. Ton

RECEIVED

JAN 02 2004

Technology Center 2100

For: SEMICONDUCTOR INTEGRATED
CIRCUIT DEVICE COMPRISING
RAM WITH COMMAND DECODE
SYSTEM AND LOGIC CIRCUIT
INTEGRATED INTO A SINGLE CHIP
AND TESTING METHOD OF THE
RAM WITH COMMAND DECODE
SYSTEM

RESPONSE TO OFFICE ACTION

Mail Stop Non-Fee Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

In response to the Office Action dated October 6, 2003, please enter the following amendments and consider the following remarks.